

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	286	345/537.ccls.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 08:50
L2	132	345/539.ccls.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 08:50
L3	511	345/536.ccls.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 08:51
L4	259	345/530.ccls.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 08:51
L5	454	345/545.ccls.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 08:51
L6	194	345/559.ccls.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 08:51
L7	161	345/556.ccls.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 08:51
L8	234	345/519.ccls.	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 08:51
L10	31678	double same buffer\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 08:51
L11	5430	double adj2 buffer\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 08:52

L12	53432	(off adj2 chip external) near2 (memory buffer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 08:52
L13	32399	display adj2 controller\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 08:53
L14	22923	frame adj2 buffer\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 08:53
L15	17748	status adj2 register\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 08:53
L18	5046	double adj buffer\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 08:56
L19	25785	display adj controller\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 08:55
L20	19579	frame adj buffer\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 08:55
L21	35510	graphic\$1 and (process\$3 accelerat\$3 generat\$3) and chip	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/17 08:56
L23	1189	18 and 12 and chip	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 08:57

L31	143	23 and 19	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 09:02
L32	128	31 and cop\$4	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 09:00
L33	121	31 and copy\$3	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 09:01
L34	115	33 and 20	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 09:02
L35	115	34 and register\$1	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 09:02
L36	106	34 and register\$1 same display	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 09:02
L38	34	18 same 19	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 09:03
L39	95	15 and 18 and 13	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 09:04
L40	291	graphic\$1 adj (process\$3 accelerat\$3 generat\$3) adj2 chip	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 09:05
L41	223	graphic\$1 adj (processor accelerator generator) adj2 chip	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 09:05
L42	153	41 and display same control\$4	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 09:05
L43	60	41 and display near2 control\$4	US-PGPUB; USPAT; USOCR; EPO	OR	ON	2005/08/17 09:05